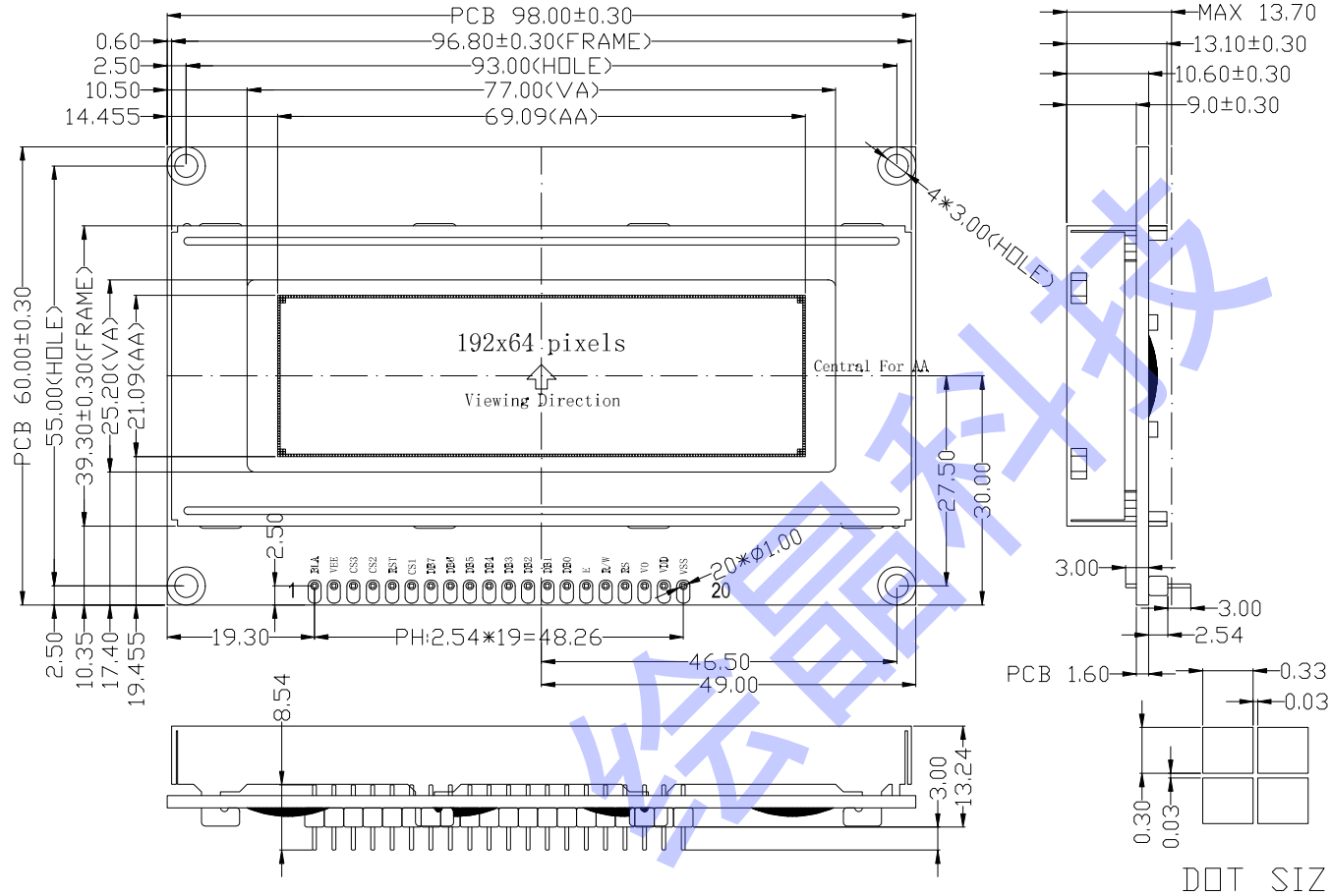
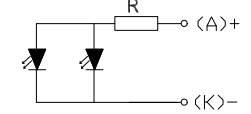


NO./编号	DESCRIPTION/修改说明	DATE/日期
△	First Release/第一版本	2023-03-11

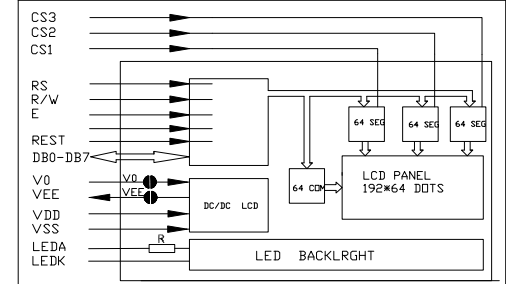
客户确认签名/
APPR. BY Customer



BL Circuit Diagram/背光原理



Block Diagram



PIN ASSIGNMENT:

1	LED+	Power supply for LED(+4.2V)
2	VEE/NC	NEGATIVE POWER OUT(Default NC)
3	CS3	Chip select (RIGHT,Low Effective)
4	CS2	Chip select (MIDDLE,Low Effective)
5	RST	Resetsignal
6	CS1	Chip select (LEFT,Low Effective)
7	DB7	Data bus line
8	DB6	Data bus line
9	DB5	Data bus line
10	DB4	Data bus line
11	DB3	Data bus line
12	DB2	Data bus line
13	DB1	Data bus line
14	DB0	Data bus line
15	E	Enable signal
16	R/W	"H"Read ; "L"Write
17	C/D	"H"Data ; "L"Instruction code
18	VO/NC	CONTRAST ADJUST(Default NC)
19	VDD	Power supply for Logic(+3.3V)
20	VSS	Ground for Logic(0V)

项目/ITEM	参数/PARAMETERS	项目/ITEM	参数/PARAMETERS
OPERATING VOLTAGE	3.3V	OPERATING TEMP	-30°C ~ 80°C
OPERATING CURRENT	15MA(max,WITHOUT BACKLIGHT)	STORAGE TEMP	-35°C ~ 85°C
LCD DRIVE VOLTAGE	9.0± 0.20V(VDD-VO / TBD)	BACKLIGHT	2 LEDS,VF=30MA(TYP.),VLED=5.0V, Y-G
DISPLAY MODE	STN(Y-G)/POSITIVE/TRANSFLECTIVE	ROHS STANDARD	YES ■ NO □
LCD DRIVE METHOD	DUTY: 1/64; BIAS: 1/9	CONNECTION	HOLE
Driver/Controller	AIP31108 *3 & AIP31107*1 OR EQV.	UNSPECIFIED	± 0.2mm
VIEW DIRECTION	6:00 O'CLOCK	Third perspective	



深圳市绘晶科技有限公司
Shenzhen huijing Technology Co. Ltd.

DRAWN/设计	LM	NAME/名称	DISPLAY MODULE
CHECKED/检查	ZPF	MODEL NO./型号	19264TXG
APPROVED/批准	YXS	REV./版本	V1.0
TEL/电话	0755-23146001	DATE/日期	2023-03-11
FAX/传真	0755-23146002	URL/网址	http://www.huijinglcm.com/
Address/地址	深圳市宝安区石岩街道官田社区工业大道东丰官田科技园4楼404室, Building 404, Huijing Guanlian Science and Technology Park, Guanlian Industrial Avenue, Shiyao Street, Bao'an District, Shenzhen		